

On the Optimal Delay Amplification Factor of Multi-Hop Relay Channels

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Overview



► Part 1: *Motivation & Intuition*

Motivation

A new metric for multi-hop relay channels: *The Delay Amplification Factor*

Preview of main results

► Part 2: *Main Results*

(Necessary) details of problem set-up

Main results & proof sketches

Work in progress & Conclusion



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Motivation



Motivation



Low latency communications



IoT



Autonomous
driving



MTC

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IMT-2020 URLLC: $\leq 1\text{ms delay}$

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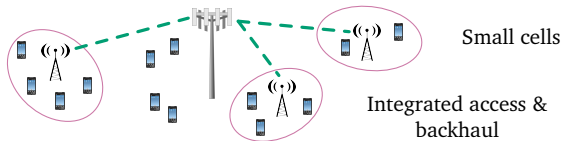
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Flexible/dense network architectures



Small cells

Integrated access & backhaul

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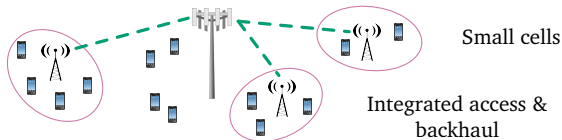
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For IT: $\text{Lots of relay channels}$

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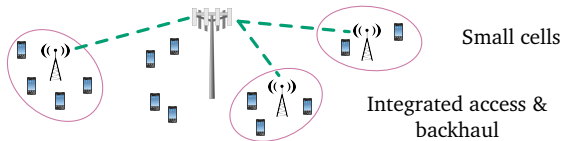


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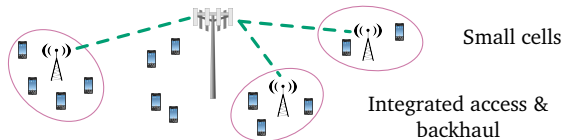
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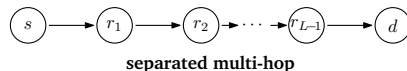
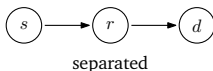
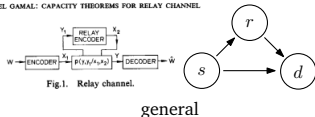


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COVER AND EL GAMAL: CAPACITY THEOREMS FOR RELAY CHANNEL

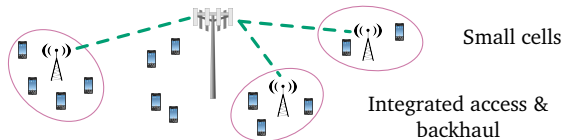


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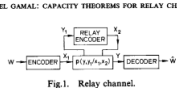


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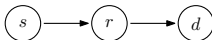
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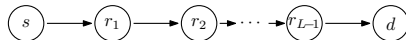
general

capacity = $\min(C_1, C_2)$



separated

capacity = $\min(C_1, C_2, \dots, C_L)$



separated multi-hop

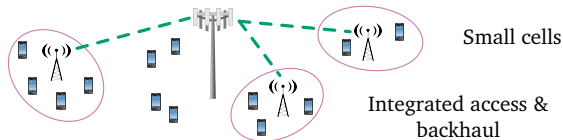
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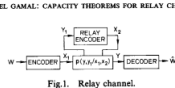


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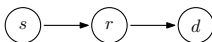
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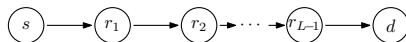
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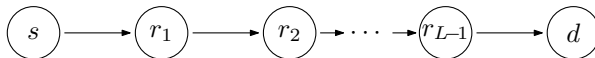
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separated multi-hop

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- This work:** When $R \rightarrow C$, what relaying schemes minimize (relative) delay?

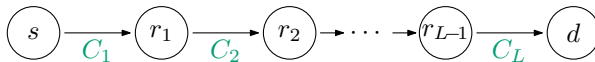
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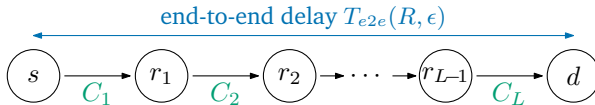


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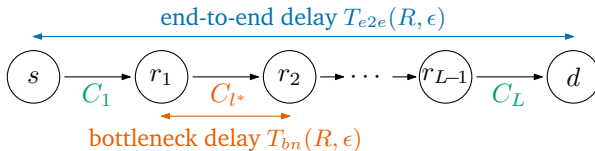
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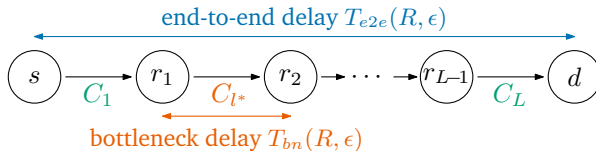
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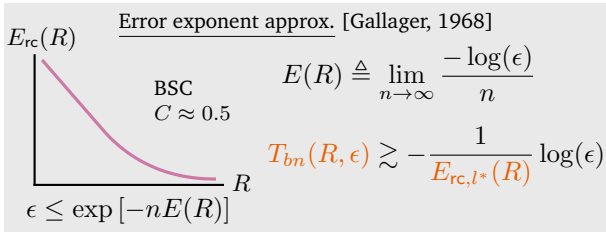
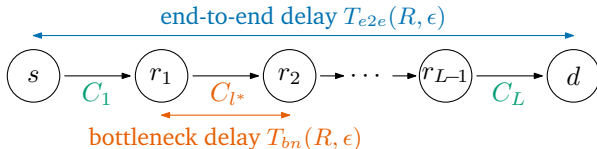


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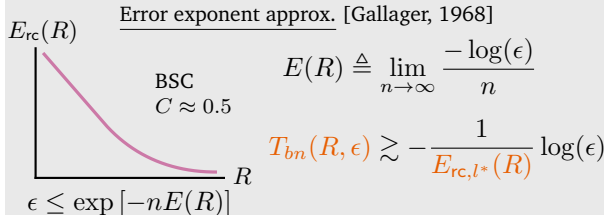
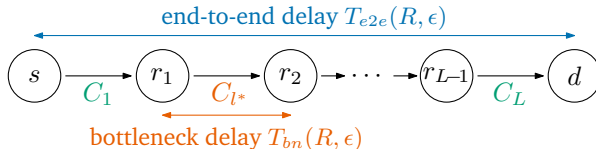
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End-to-end error exponent

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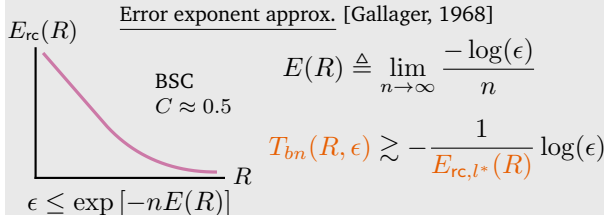
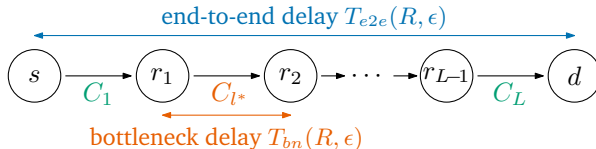
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$E_{\Phi}(R)$: “Error exponent of relaying scheme Φ ”

- Coding scheme
- Channel transition probabilities
- Operation at relays

} need more definitions

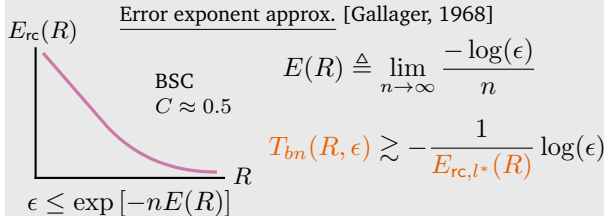
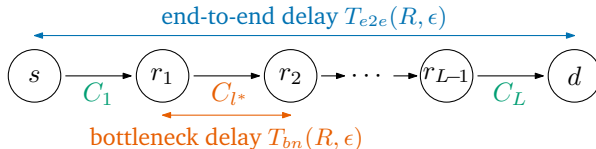
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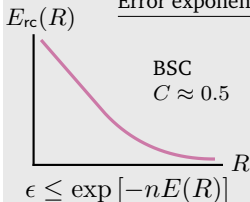
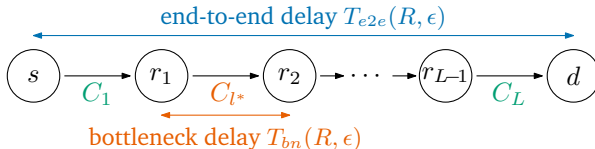
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Error exponent approx. [Gallager, 1968]

$$E(R) \triangleq \lim_{n \rightarrow \infty} \frac{-\log(\epsilon)}{n}$$

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*Small Lemma:
 $\text{DAF}_{\Phi} \geq 1 \quad \forall \Phi$

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Preview of main results



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We define the *Delay Amplification factor* of an L -hop relaying scheme Φ as

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1. Regardless of the relaying scheme, we always have $\text{DAF} \geq 1$
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Main results

1. A relaying scheme where $\text{DAF}_{\Phi} = 1$ if $l^* = L$
2. A 1-bit stop feedback relaying scheme where $\text{DAF}_{\Phi'} = 1$ regardless of the position of l^*

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Starting times, relay encoding & joint decoding

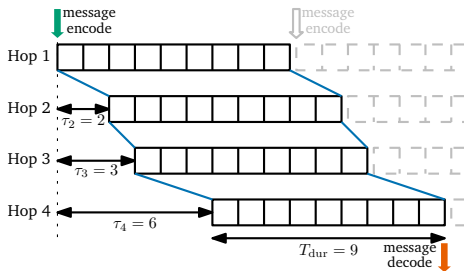
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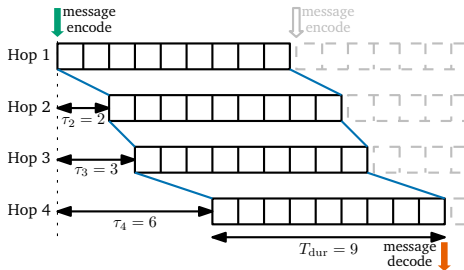


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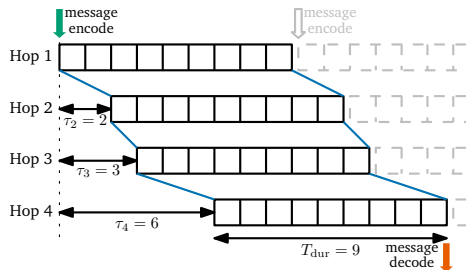
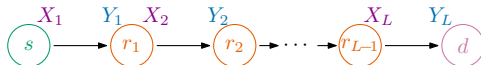


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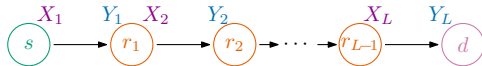


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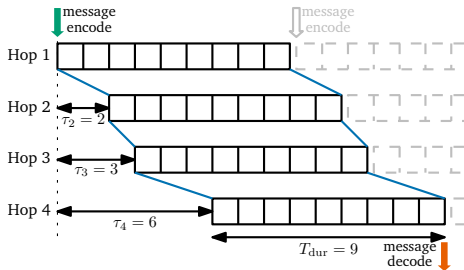
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- ▶ Block encoding @ source

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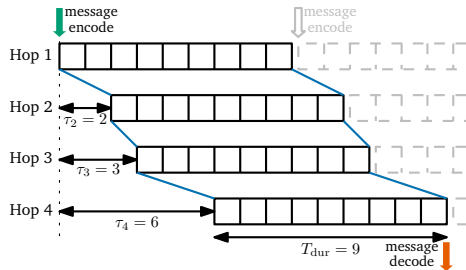
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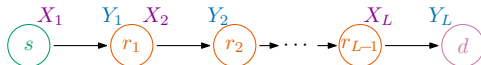


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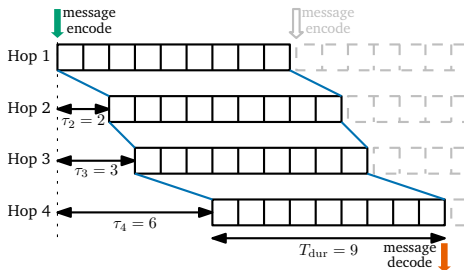
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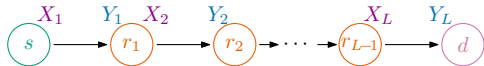


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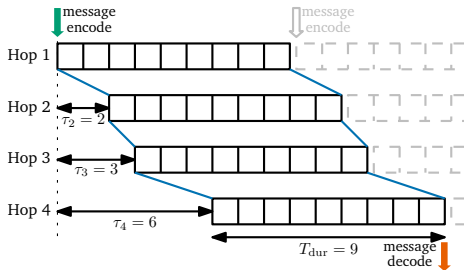
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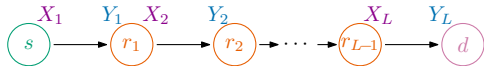
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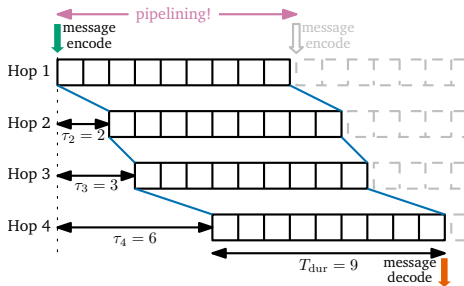
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Pipelining:

Source sends next message before previous message is received by destination

Error exponent of relaying scheme Φ



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$$(i) \quad T \geq \tau_L + T_{\text{dur}}$$

$$(ii) \quad R \leq \frac{\log(|\mathcal{M}|)}{T_{\text{dur}}}$$

$$(iii) \quad \epsilon \geq P(M \neq \widehat{M})$$

► Let $\mathcal{A}_\Phi \triangleq \{\text{all } (T, R, \epsilon) \text{ achievable by } \Phi\}$

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- ▶ Let $\mathcal{A}_\Phi \triangleq \{\text{all } (T, R, \epsilon) \text{ achievable by } \Phi\}$
- ▶ Then define the error exponent of an L-hop relaying scheme Φ as

$$E_\Phi(R) \triangleq \limsup_{T \rightarrow \infty} \sup_{\epsilon: (T, R, \epsilon) \in \mathcal{A}_\Phi} \frac{-\log(\epsilon)}{T}$$

Aside: decode-&-forward, amplify-&-forward

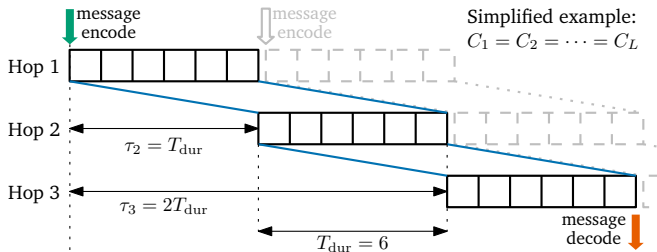


Definitions & analysis framework captures many possible relaying schemes, examples:

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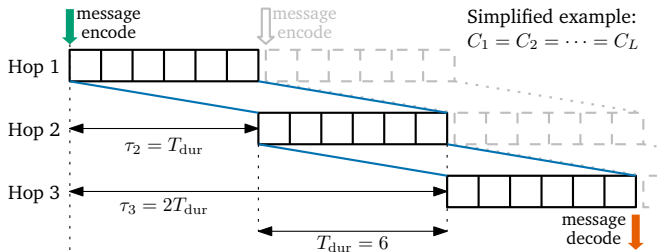


- ▶ Random coding over every hop
- ▶ T_{dur} is the maximum of the block lengths

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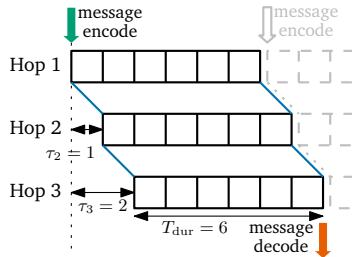
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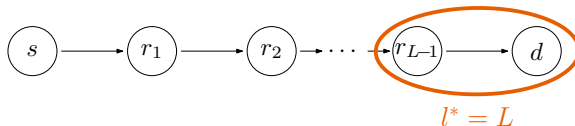


- ▶ Random coding between source and destination
- ▶ Relays forward received symbols

Main Result 1: Feedback-free setting



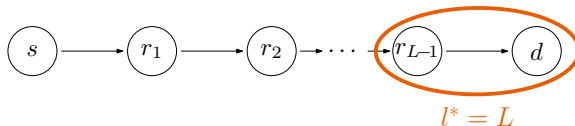
Main Result 1: Feedback-free setting



In the feedback-free setting, **if the bottleneck hop is the last hop** (i.e. $l^* = L$), then transcoding achieves

$$\text{DAF}_{\text{TC}} = 1 \leq \text{DAF}_{\Phi}, \forall \Phi.$$

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$$\text{DAF}_{\text{TC}} = 1 \leq \text{DAF}_{\Phi}, \forall \Phi.$$

Proof includes:

- ▶ Description of coding scheme (source, relays, destination)
- ▶ Derivation of error probability & delay \Rightarrow Error exponent \Rightarrow DAF

Feedback-free setting — proof outline I

To reduce delay: Break codewords into K micro-blocks, each of length Δ symbols

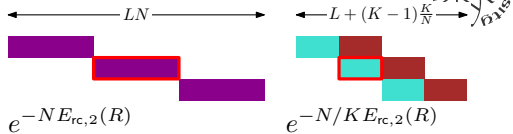
- ▶ Each protected by random coding with rate C_L
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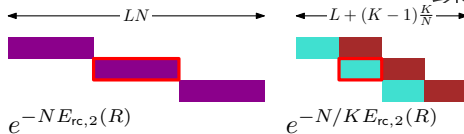


Problem: Error explodes!

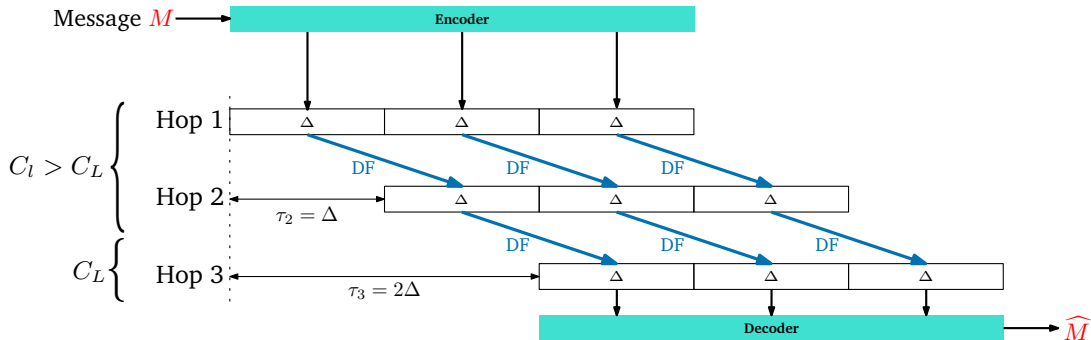
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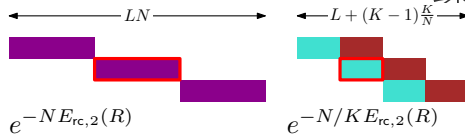


(Example: $L = 3, K = 3$)

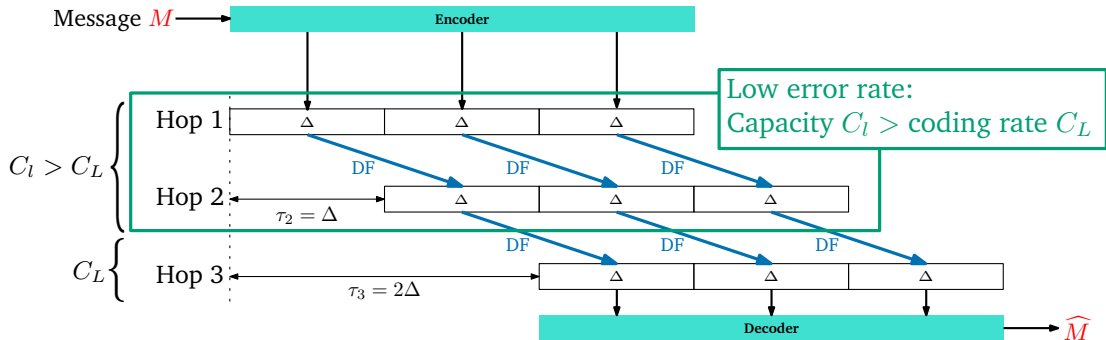
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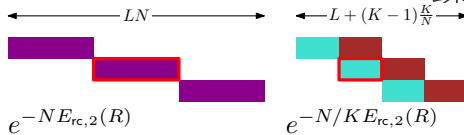


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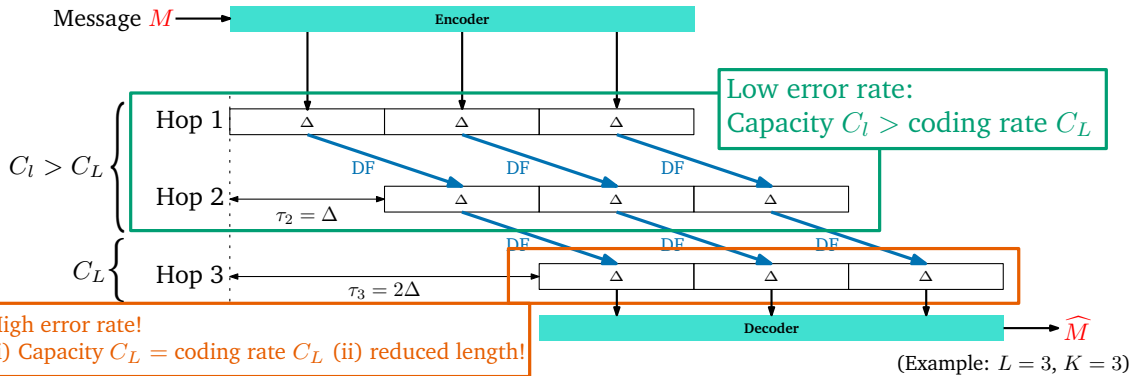
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Feedback-free setting — proof outline II

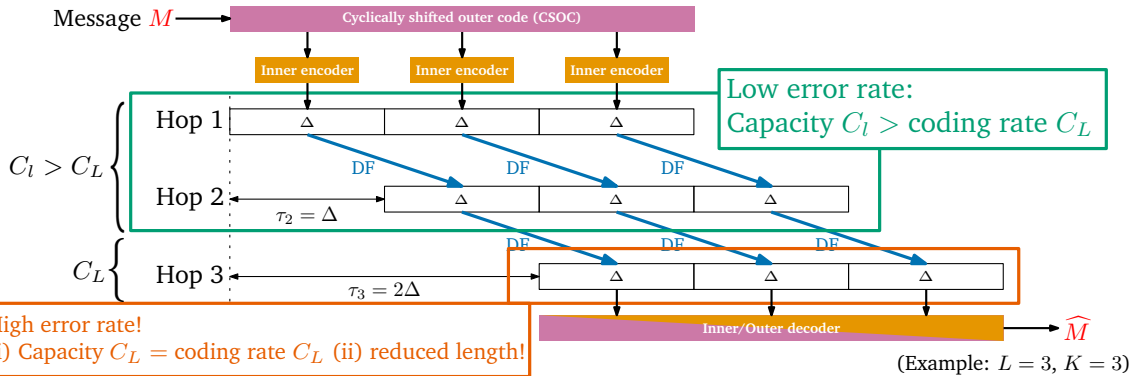
Protect message over last hop using additional coding

- ▶ Inspired by concatenated coding
- ▶ CSOC outer code + random codes for micro-blocks
- ▶ **Maximum-likelihood decoding at destination**

Cyclically shifted outer code (CSOC):

$$\{(\mathbf{c}_{i_1}^{[1]}, \dots, \mathbf{c}_{i_K}^{[K]}) : \sum_{k=1}^K i_k \bmod e^{K\Delta(R_I - R)} = 0\}$$

Number of messages: $e^{K\Delta R_I} / e^{K\Delta(R_I - R)} = e^{K\Delta R}$



Feedback-free setting — proof outline II

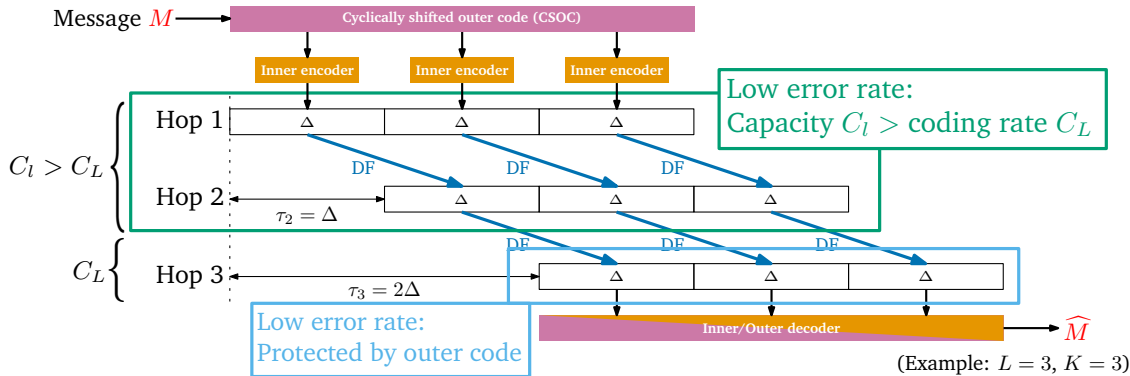
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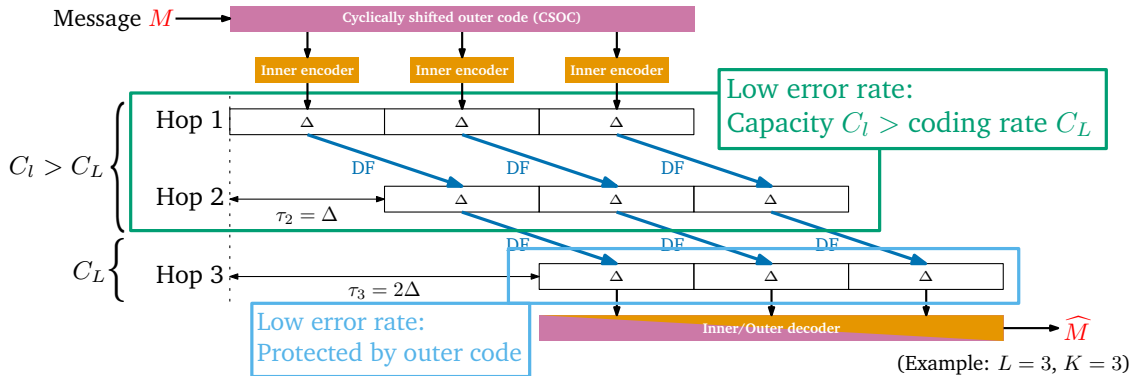
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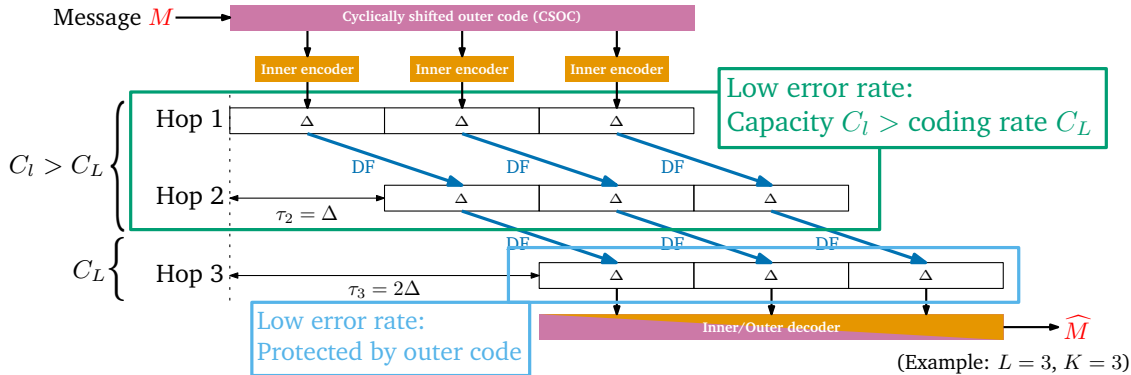
Feedback-free setting — proof outline III

- Choose K such that
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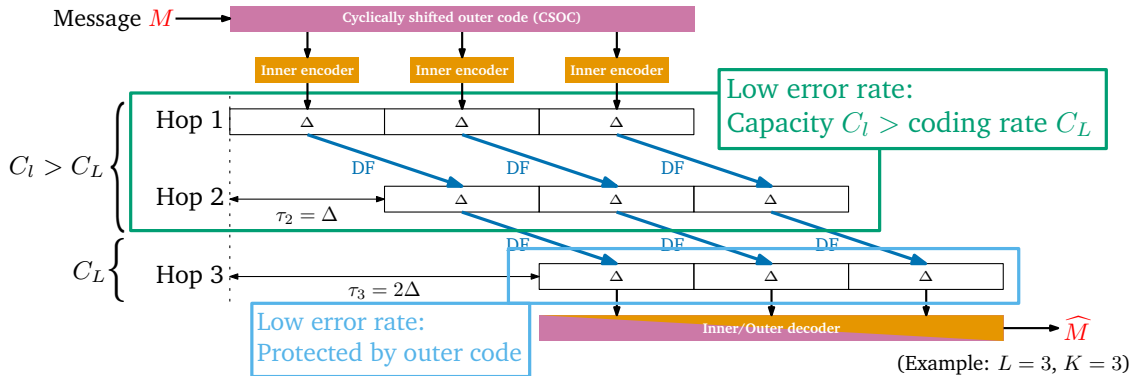
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► Then $E_{TC}(R) = \frac{K}{K+L-1} E_{rc,L}(R)$



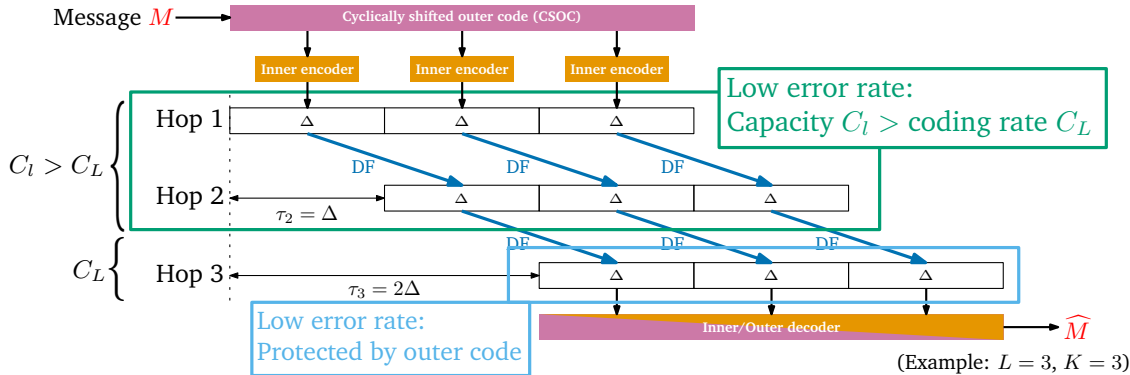
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Then, when $R \rightarrow C_L$, $K \rightarrow \infty$ and we get

$$E_{TC}(R) = E_{rc,L}(R) \quad \blacksquare$$



Main Result 2: Stop feedback setting



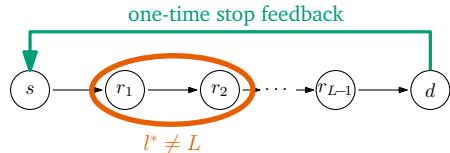
Main Result 2: Stop feedback setting

Some new definitions:

- ▶ Allow T_{dur} to be a stopping time, use $E[T_{\text{dur}}]$
- ▶ Use random coding error exponent with stop feedback [Polyanskiy, Poor, Verdu 2011]

$$E_{\text{sf},l}(R) = (C_l - R)^+$$

▶ Then $\overline{\text{DAF}}_{\Phi} \triangleq \lim_{R \nearrow C_{l^*}} \frac{E_{\text{sf},l}(R)}{E_{\Phi}(R)} = \lim_{R \nearrow C_{l^*}} \frac{C_{l^*} - R}{E_{\Phi}(R)}$



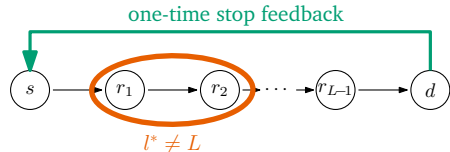
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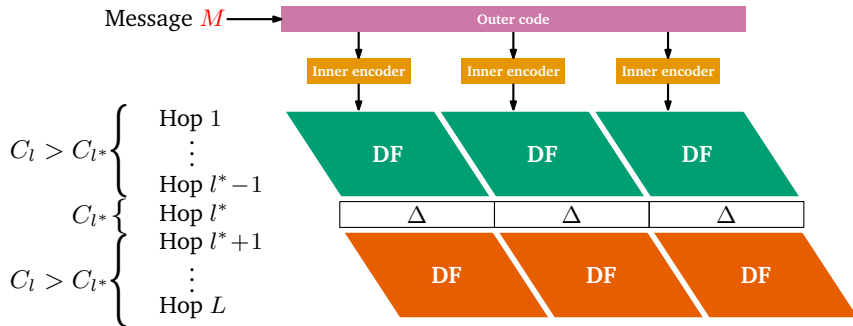
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In the stop feedback setting, **regardless of the position of the bottleneck link**, transcoding achieves

$$\overline{\text{DAF}}_{\text{TC}} = 1$$

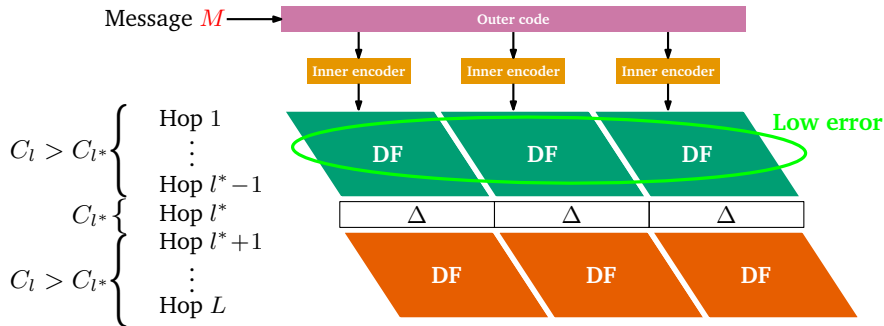
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Stop feedback setting — proof outline



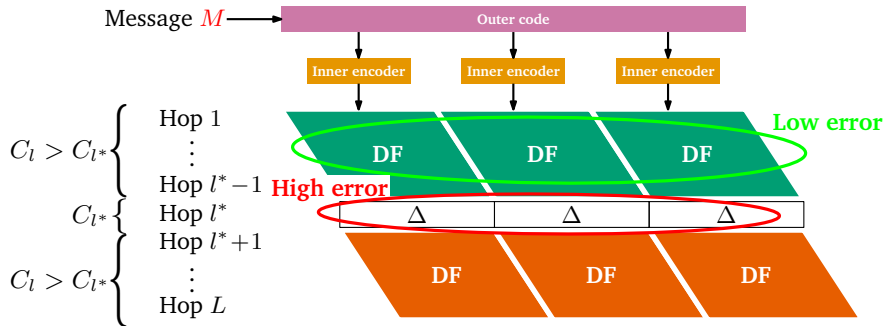
- When $l^* \neq L$, all post-bottleneck hops have high error rate due to error propagation

Stop feedback setting — proof outline



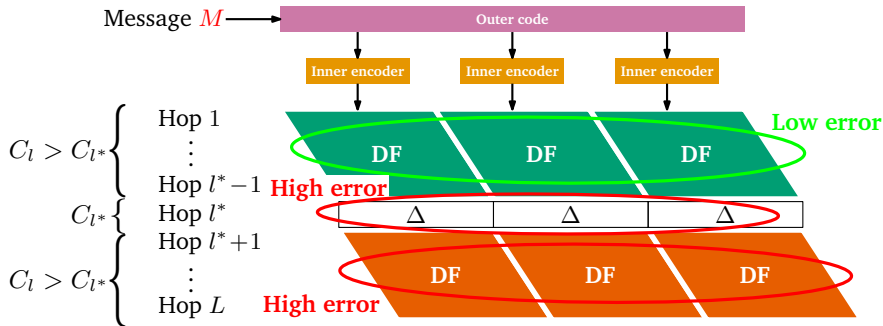
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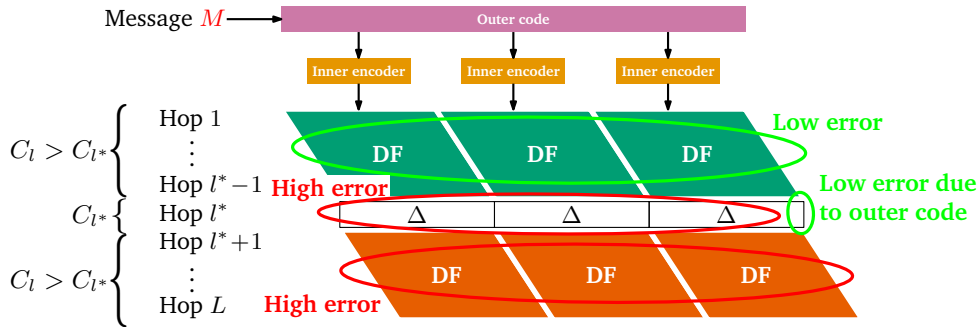


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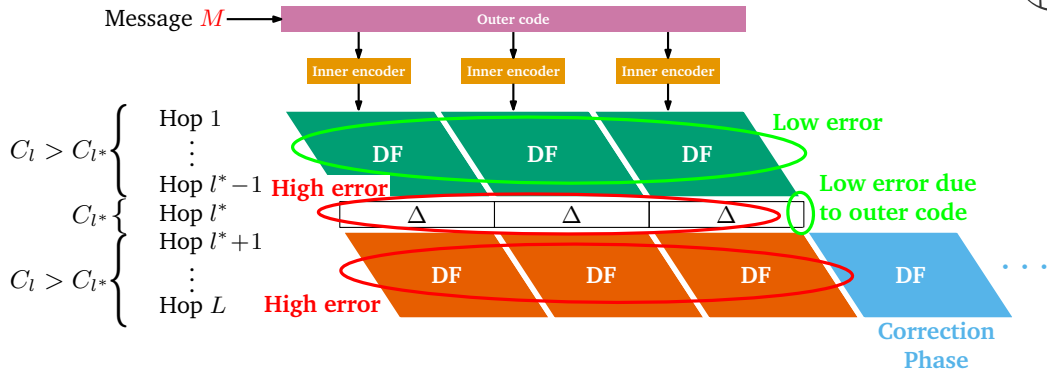


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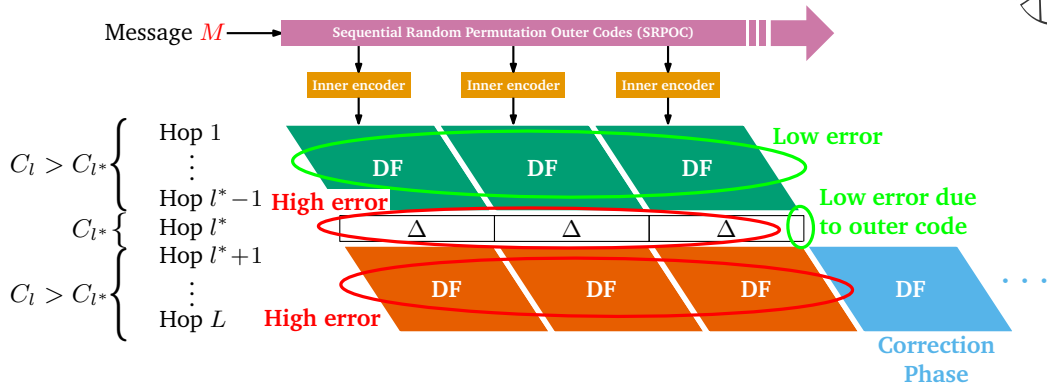
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- ▶ Bottleneck relay (BR) can decode outer code after K micro-blocks

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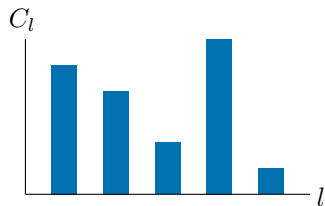
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- ▶ Bottleneck relay (BR) can decode outer code after K micro-blocks
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- ▶ With careful code construction can show $\overline{\text{DAF}}_{\text{TC}} = 1$

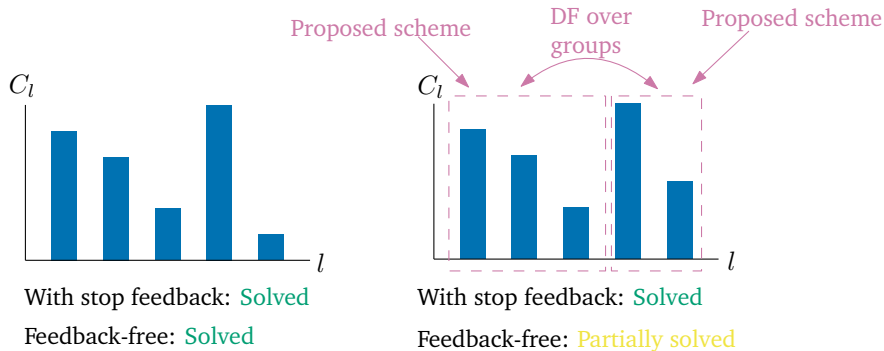
Work in progress



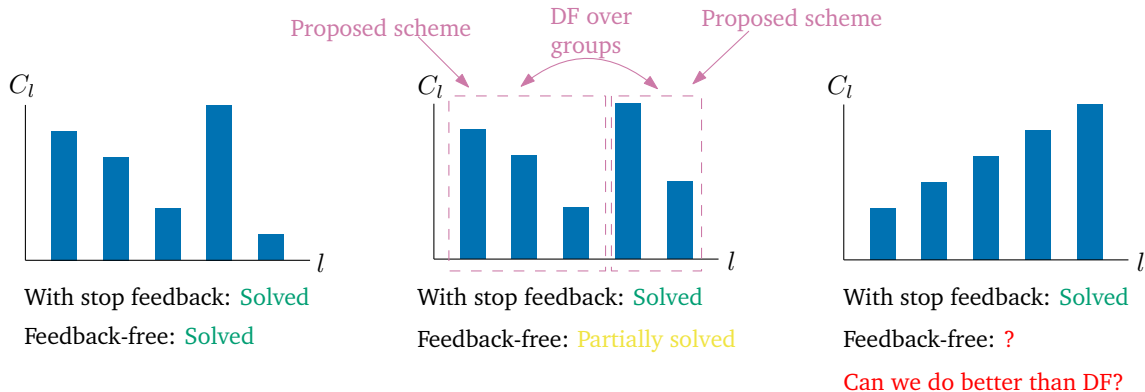
With stop feedback: Solved

Feedback-free: Solved

Work in progress



Work in progress



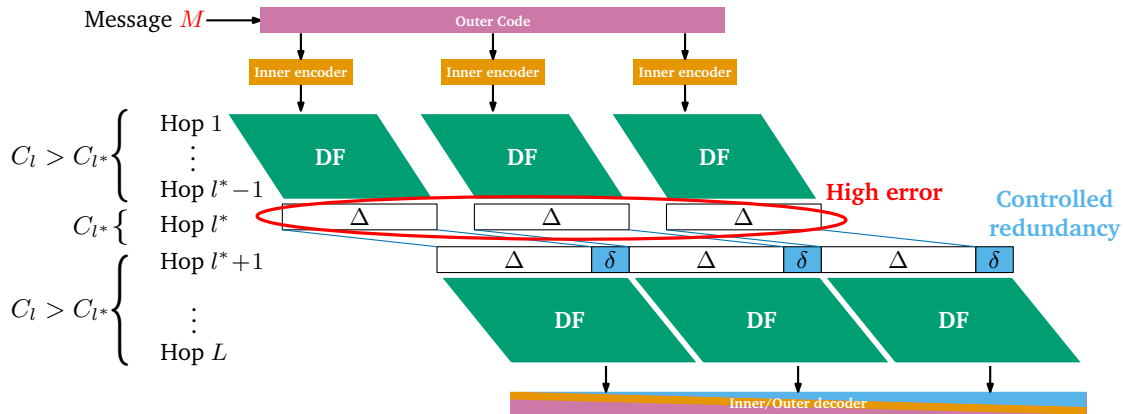
Work in progress

What is left? — Case $l^* \neq L$ without feedback



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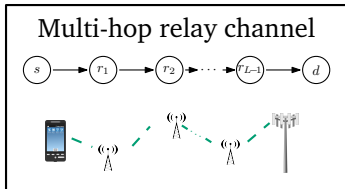


- BR adds redundancy per micro-block
- Conjecture: $1 < \text{DAF}_{\text{TC}'} < \text{DAF}_{\text{DF}}$

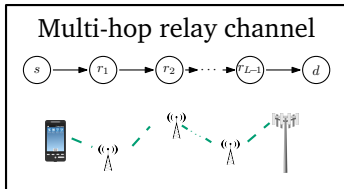
Conclusion



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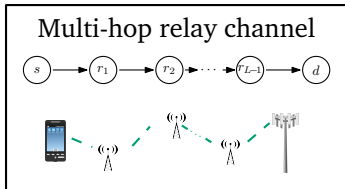


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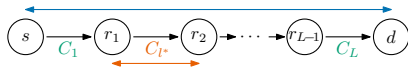


When $R \rightarrow C$, what relaying schemes minimize relative delay?

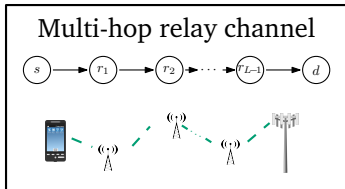
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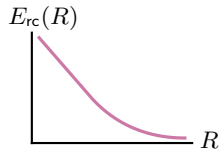
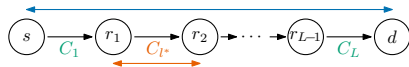
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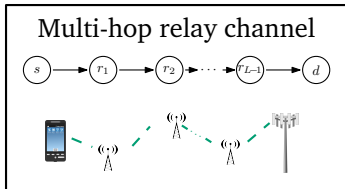
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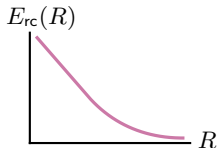
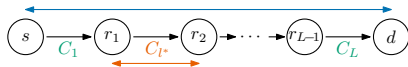
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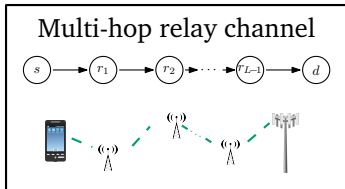
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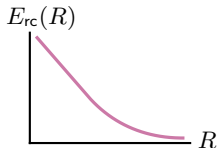
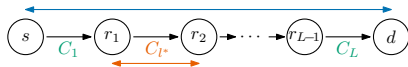
Delay Amplification Factor

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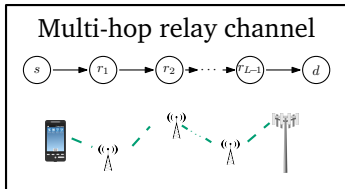
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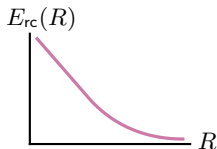
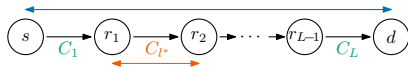
Main Takeaways

- ▶ Relaying schemes with $\text{DAF} = 1$ are possible
- ▶ Linearly growing delay of decode-&-forward is **NOT** a fundamental limit

Conclusion



When $R \rightarrow C$, what relaying schemes minimize relative delay?



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Future Work

- ▶ $l^* \neq L$ without feedback
- ▶ Applications to Gaussian, Rayleigh fading channels

Questions.



References I



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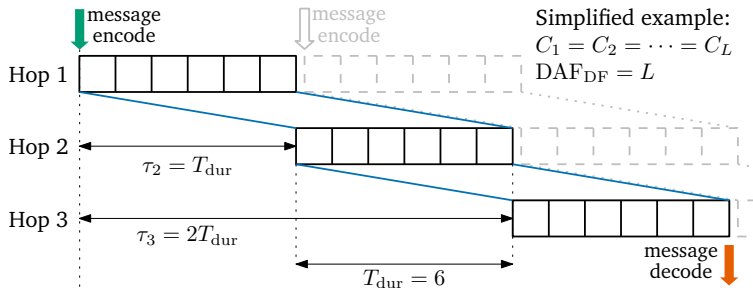
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Backup Slides.

Aside: DAF of decode-&-forward



- ▶ Operate each hop close to capacity $\Rightarrow T \approx \sum_l \frac{B}{C_l}$ for a B -bit message
- ▶ But $T_{bn} \approx \frac{B}{C_{l^*}}$
- ▶ Then $\text{DAF}_{\text{DF}} = \frac{T}{T_{bn}} = \sum_l \frac{C_{l^*}}{C_l} > 1$

N.B.: AF does not achieve capacity, DAF does not apply

Prior Work: Transcoding

